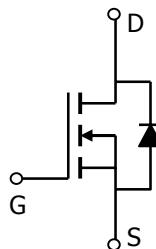
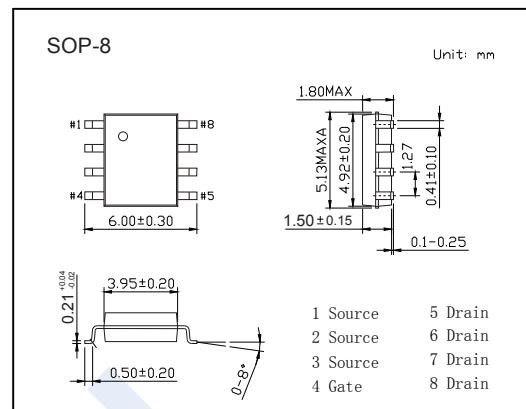


## N-Channel MOSFET

AO4438 (KO4438)

## ■ Features

- $V_{DS} (V) = 60V$
- $I_D = 8.2 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 22m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 27m\Omega (V_{GS} = 4.5V)$

■ Absolute Maximum Ratings  $T_a = 25^\circ C$ 

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	8.2	A
		6.6	
Pulsed Drain Current	$I_{DM}$	40	W
Power Dissipation	$P_D$	3.1	
		2	
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	40	$^\circ C/W$
		75	
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	30	$^\circ C$
Junction Temperature	$T_J$	150	
Storage Temperature Range	$T_{stg}$	-55 to 150	

## N-Channel MOSFET

### AO4438 (KO4438)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μ A, V <sub>GS</sub> =0V	60			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>D</sub> =48V, V <sub>GS</sub> =0V			1	μ A
		V <sub>D</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>D</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>D</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μ A	1		3	V
Static Drain-Source On-Resistance	R <sub>D(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8.2A			22	m Ω
		V <sub>GS</sub> =10V, I <sub>D</sub> =8.2A T <sub>J</sub> =125°C			40	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =7.6A			27	
On State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =5V	40			A
Forward Transconductance	g <sub>FS</sub>	V <sub>D</sub> =5V, I <sub>D</sub> =8.2A		24		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =30V, f=1MHz		1920	2300	pF
Output Capacitance	C <sub>oss</sub>			155		
Reverse Transfer Capacitance	C <sub>rss</sub>			116		
Gate Resistance	R <sub>G</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =0V, f=1MHz		0.65	0.8	Ω
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =30V, I <sub>D</sub> =8.2A		47.6	58	nC
Total Gate Charge (4.5V)				24.2	30	
Gate Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =30V, I <sub>D</sub> =8.2A		6		ns
Gate Drain Charge	Q <sub>gd</sub>			14.4		
Turn-On DelayTime	t <sub>d(on)</sub>			8.2		
Turn-On Rise Time	t <sub>r</sub>			5.5		
Turn-Off DelayTime	t <sub>d(off)</sub>	I <sub>F</sub> = 8.2A, dI/dt= 100A/ μ s		29.7		ns
Turn-Off Fall Time	t <sub>f</sub>			5.2		
Body Diode Reverse Recovery Time	t <sub>rr</sub>			34	41	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 8.2A, dI/dt= 100A/ μ s		53		nC
Maximum Body-Diode Continuous Current	I <sub>s</sub>				3	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>s</sub> =1A, V <sub>GS</sub> =0V			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

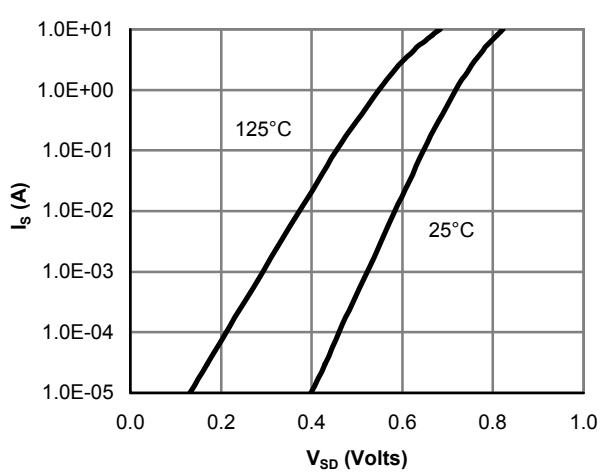
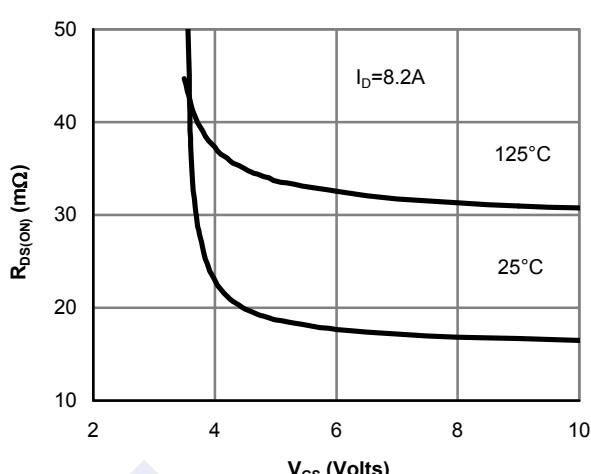
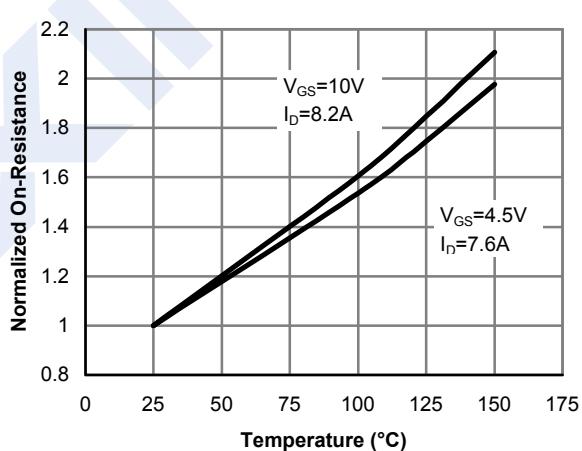
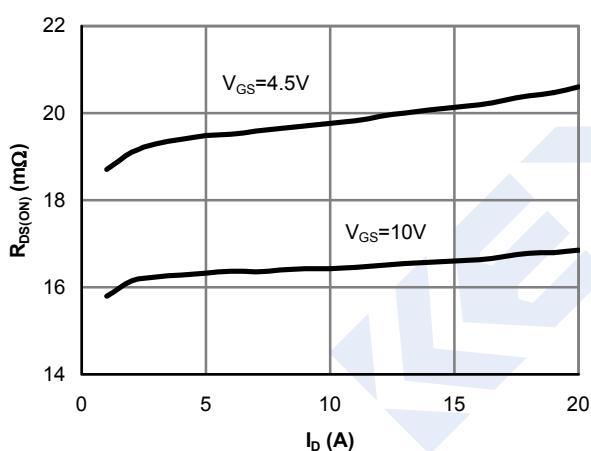
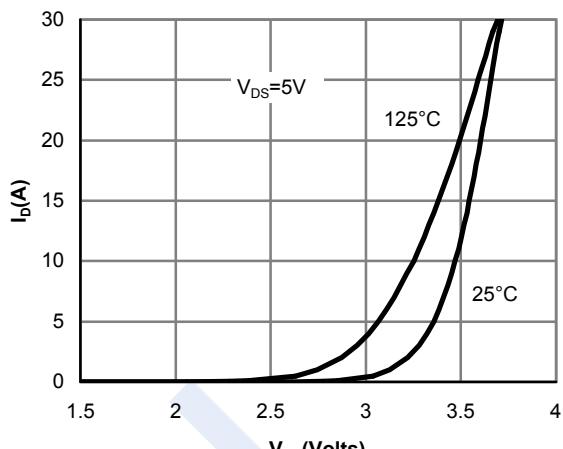
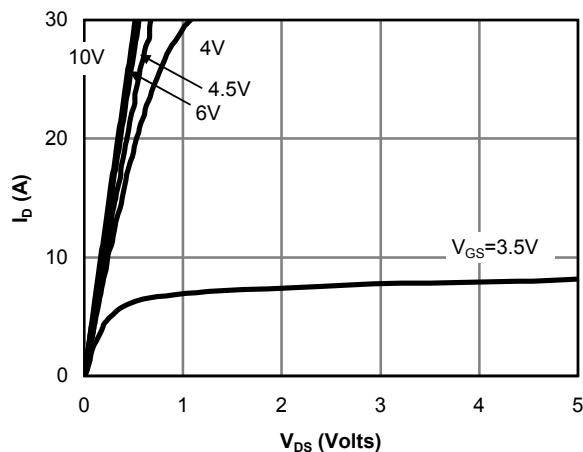
■ Marking

Marking	4438 KC***
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## N-Channel MOSFET

### AO4438 (KO4438)

#### ■ Typical Characteristics



## N-Channel MOSFET

### AO4438 (KO4438)

#### ■ Typical Characteristics

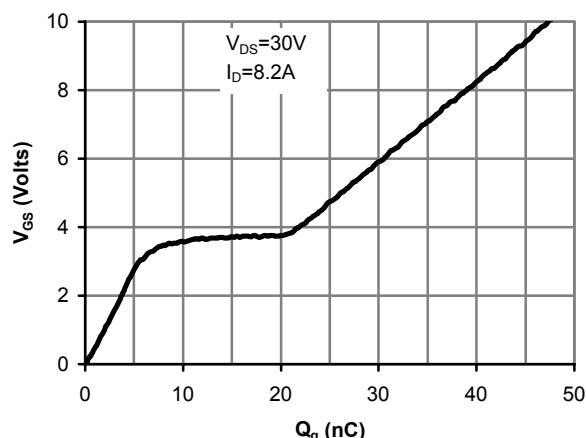


Figure 7: Gate-Charge Characteristics

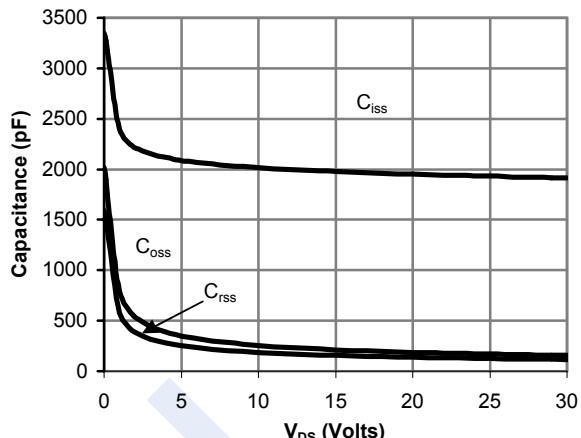


Figure 8: Capacitance Characteristics

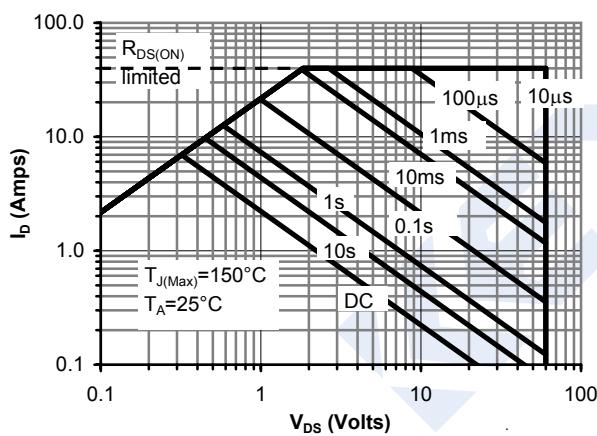


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

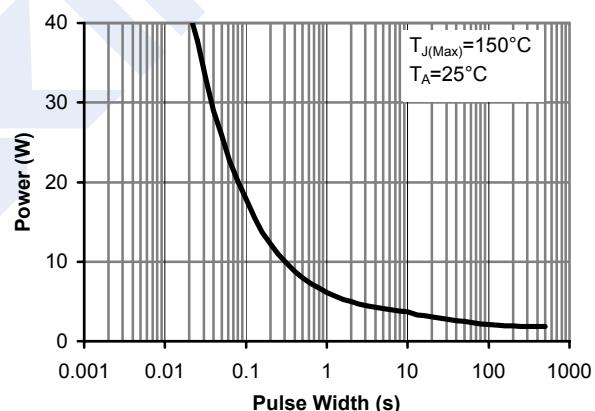


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

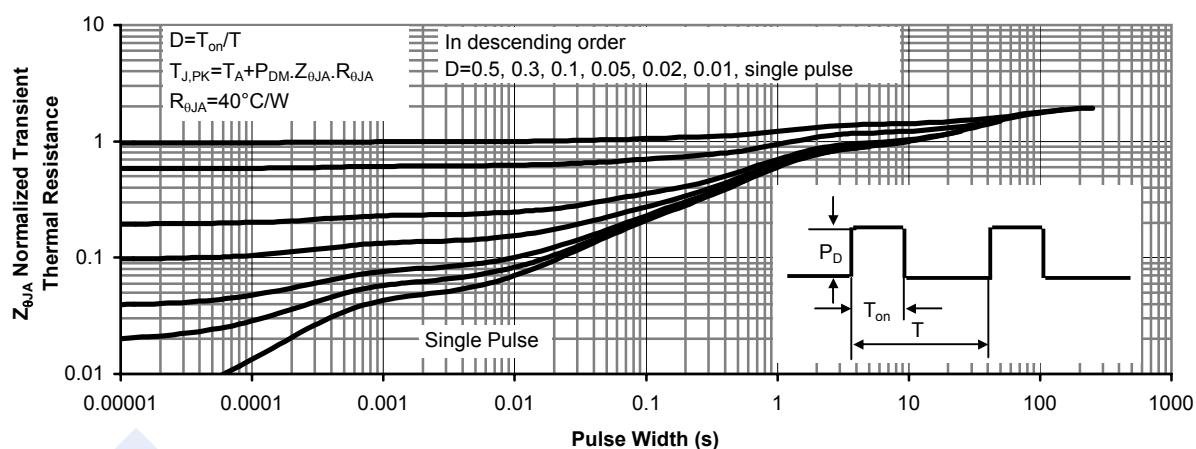


Figure 11: Normalized Maximum Transient Thermal Impedance